GEORGIA INSTITUTE OF TECHNOLOGY

SCHOOL of ELECTRICAL & COMPUTER ENGINEERING

ECE 3710 Circuits and Electronics

GTL - Syllabus & Schedule

Instructor: Dr. Suresh Sundaram,

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Office: GTL 213

Office Hours:

Walk-ins, or by appointment – specific hour TBA latter

Class Details:

Lecture: TBA

Co-requisites:

Phys 2212/2232

Course Websites:

http://canvas.gatech.edu/

Course Materials:

Textbook(s): The Textbook Circuits by Fawwaz Ulaby & Michel Maharbiz. Available at Barnes and Noble and at http://www.studica.com/GeorgiaTech.html

Other Materials:

NI myDAQ and Circuits Textbook Bundle (Includes NI myDAQ student data acquisition board

Note: The myDAQ and circuit parts are available at GTL. Contact me if you want to use them outside of class. **The first lab will be the second week of class.**

Attendance Policy:

Attendance is mandatory. Any unexcused absence will result in a grade of zero for both the In-class quiz and In-class worksheets for that period. In-class quizzes are administered during the first 10 minutes of the period. Students who arrive after the quiz is given will receive a grade of zero for that quiz. There will be no In-class quiz during the scheduled In-class labs hours to accommodate successful completion of the lab works.

According to the Institute policy, you can take a hybrid course completely remote if you are an international student who cannot travel respective institute/campuses or if you have a written accommodation that excuses you from attending in-person classes.

Grade Policy:

10% – Homework (completed on Canvas)

10% – In-Class Quizzes (Lowest 3 dropped)

5% – In-Class Worksheet (Participation, one dropped)

15% – In-Class Labs

20% – Exam 1 (Week of ……)

20% – Exam 2 (Week of …..)

20% – Final Exam

2% –Build a useful circuit and give an in-class demo.

Course Structure:

This course is divided into three mini-courses:

- Linear Circuits 1: DC Analysis
- Linear Circuits 2: AC Analysis
- Introduction to Electronics

All of the video lectures for the semester will be on-line uploaded in Canvas.

The homework will be completed on-line (canvas).

The assigned lectures for each class period will be listed on the syllabus schedule in the Canvas site. Unless otherwise noted, the GTL course will follow the schedule posted here. There will be daily quizzes in class on the on-line lecture material. Quizzes are open-note, but closed-Internet (and closed-neighbor). You may need a calculator for some of the quizzes.

There will be several in-class labs where students will perform hands-on activities using data acquisition boards. Some of these activities include exploration or RC and RLC

circuits, op-amp circuits, filters, and physically-motivated applications of electronic circuits. These hands-on activities are designed for students to complete during class and turn in a worksheet.

There will be common tests among all sections and a common final exam. Class time will be devoted to the hands-on activities, working sample problems, recitation, and working homework.

Final Exam:

The final exam for this course will be held during the exam period assigned for the class.

Dead Week:

There may be homework and an in-class lab during the last week of the semester (dead week). (Note: This in-class lab is allowable under the Georgia Tech dead week policy since the scope and workload of this in-class activity does not reach the level of a standard lab.)

Lab Software:

We will use the ELVISmx Instrument Launcher for the myDAQ device. This software is Windows-based, so please install it on a Windows machine or the Windows partition of a Mac. (Use Bootstrap or Parallels with this software.) The software is available at the Na- tional Instruments site (http://joule.ni.com/nidu/cds/view/p/id/2157/lang/en/). Support for the myDAQ device can be found at http://www.ni.com/tutorial/11420/en/.

WARNING: It may take over an hour to download and install the software.

Topical Outline:

Resistive Circuits

- Components
- Ohm's Law
- Resistors in parallel, series
- Kirchhoff's Current and Voltage Laws
- Voltage divider and current divider laws
- Thévenin Equivalent Circuits
- Superposition

Reactive Circuits

- Inductors and Capacitors

- Parallel and series connections of inductors and capacitors
- Transient Analysis of First-Order circuits

Frequency Analysis of Circuits

- Steady-state sinusoidal analysis and impedance
- Transfer function
- Bode plots
- Filtering

Power in AC Circuits

- Real, reactive, and apparent power
- Power factor

Fundamental Devices in Electronics

- Ideal diodes
- Simple piecewise linear model of diode
- MOS Field-Effect Transistors
- Operational Amplifiers

Electronic Applications

- Rectifiers
- Amplifiers
- Active Filters
- Logic Gates (and introduction to Boolean algebra logic)

Course Objectives:

The objectives of this course are to teach students

- To analyze circuits that contain resistors, capacitors, and inductors with direct current and alternating current sources.
- To analyze circuits in the time domain showing transient response and in the frequency domain showing filtering and resonance properties.
- To be familiar with nonlinear circuit components and practical circuits can be built from these components.

Learning Outcomes:

At the completion of the course, the students should be able to

- Determine voltages and currents in a resistive network.
- Sketch the transient response of RC and RL circuits and be familiar with the standard transient responses of RLC circuits.
- Use complex phasors to determine the steady-state responses of sinusoidal sources voltages or currents.

- Understand and analyze the frequency response characteristics of filters
- Analyze power characteristics in reactive circuits.
- Build and test real circuits containing RLC components, op amps, diodes, and transistors.
- Design and build simple filters, rectifiers, and amplifiers

Collaboration:

Students may discuss assignments in general terms with one another, but (unless stated otherwise) all work should be generated individually. Likewise, students may receive assistance on assignments from the course instructors. However, all of the assignments in this course are to be completed individually. Copying or allowing peers to copy all or portions of any assignment is considered plagiarism and is expressly forbidden.

Academic Misconduct:

All students taking this course are required to strictly adhere to the Georgia Tech Honor Code, whose complete text may be found at http://honor.gatech.edu/content/2/the-honor-code. Any violations of the Code are considered academic misconduct and will be submitted to the Office of the Dean of Students for appropriate action.