Instructor:
Simon Gautier
simon.gautier@georgiatech-metz.fr (Please include “ECE 2031” in the subject line.)

Office hours
Institut Lafayette, Office 103
Wednesday 9:30-11:30
Other times by appointment in person or remote.

Class Details:
Lecture (Grey Room): TBD
Lab (Institut Lafayette - Level 0): TBD

Prerequisites:
ECE 2020
ECE 2035 or 2036 (may be taken concurrently)

Course Materials:
Required Text:
Thomas R. Collins and Christopher M. Twigg
ISBN: 978-0757571572
An eBook option is available directly from the publisher.
You must purchase a new copy. (There are consumable pages.)

Optional Texts:
H. O. Hamblen, T. S. Hall, and M. D. Furman
ISBN: 978-0387726700
Leslie C. Perelman, James Paradis, and Edward Barrett
The Mayfield Handbook of Technical & Scientific Writing
ISBN: 978-1559346474
ECE 2031 Workbook (available at the main course website)
Attendance Policy:

Attendance is expected for the lecture remote and live sessions and mandatory for all laboratory sessions. Any absence from an exam or laboratory session will result in a grade of zero, which may be made up at the discretion of the instructor.

Grade Policy:

- 25% Lab reports
- 25% Prelab quizzes
- 10% One or more Lab Practical Excercises
- 20% Final Project
- 20% In-class Exam

All letter grade assignments are made by the instructor and are based on the ranking in each individual laboratory session. All assignments are individual assignments. This includes pre-lab work, laboratory reports, homework assignments, computer simulations, and exams. You will work in groups on the labs, but everything you turn in must be your own work.

The written exams are closed book and closed note. The lab practical exam is open book, open notes. The lab practical exam will be determined later in the semester. At the beginning of all exams, any electronic communication device must be turned off and put away for the duration of the exam.

Open Lab:

Hopefully you will complete all lab assignments during your allotted time. I have yet to decide whether or not it will be necessary to hold additional open lab hours.

Final Exam and Dead Week:

There is no final exam for this course. You will give a final project presentation during the lab session of dead week. The written final report will be due during finals week.

Academic Misconduct:

All students taking this course are required to strictly adhere to the Georgia Tech Honor Code, whose complete text may be found at http://honor.gatech.edu/content/2/the-honor-code. Any violations of the Code are considered academic misconduct and will be submitted to the Office of the Dean of Students for appropriate action.

Collaboration:

Students may discuss assignments in general terms with one another, but all work should be generated individually. Likewise, students may receive assistance on assignments from the course instructors. However, all of the assignments in this course are to be completed individually. Although there are laboratory partners, each assignment—laboratory reports, homework problems, exams—must reflect only the
efforts of the student whose name appears on the assignment. Copying or allowing peers to copy all or portions of any assignment is considered plagiarism and is expressly forbidden.

**Important dates:**
- First day of class: TBD
- Last day of class: TBD
- Recess week: TBD

**Tentative schedule**

<table>
<thead>
<tr>
<th>Week</th>
<th>Lecture</th>
<th>Lab</th>
</tr>
</thead>
<tbody>
<tr>
<td>#1</td>
<td>Introduction</td>
<td>No lab</td>
</tr>
<tr>
<td>#2</td>
<td>Computer Aided Logic Design</td>
<td>Lab 1</td>
</tr>
<tr>
<td>#3</td>
<td>Discrete Circuits</td>
<td>Lab 2</td>
</tr>
<tr>
<td>#4</td>
<td>Circuit Characteristics</td>
<td>Lab 3</td>
</tr>
<tr>
<td>#5</td>
<td>State Machines (SMs) and VHDL I</td>
<td>Lab 4</td>
</tr>
<tr>
<td>#6</td>
<td>Review session / written exam</td>
<td>No Lab</td>
</tr>
<tr>
<td>#7</td>
<td>VHDL II and Sequential Timing</td>
<td>Lab 5</td>
</tr>
<tr>
<td>#8</td>
<td>Train Lab</td>
<td>Lab 6</td>
</tr>
<tr>
<td>#9</td>
<td>Simple Computer I</td>
<td>Lab 7</td>
</tr>
<tr>
<td>#10</td>
<td>Simple Computer II</td>
<td>Lab 8</td>
</tr>
<tr>
<td>#11</td>
<td>Practical Exam Review</td>
<td>Practical exam</td>
</tr>
<tr>
<td>#12</td>
<td>Final Project Introduction</td>
<td>Project work</td>
</tr>
<tr>
<td>#13</td>
<td>Proposals</td>
<td>Project work</td>
</tr>
<tr>
<td>#14</td>
<td>Written Exam Review</td>
<td>Design summary/project work</td>
</tr>
<tr>
<td>#15</td>
<td><strong>In class Exam</strong></td>
<td>Project work</td>
</tr>
<tr>
<td>#16</td>
<td>Presentation &amp; Report Tips</td>
<td>Project demos/project work</td>
</tr>
</tbody>
</table>